



Atty. Dkt. No. 039153-0256 (F0113)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Yu

Title: A PROCESS FOR  
MANUFACTURING  
TRANSISTORS HAVING  
SILICON/GERMANIUM CHANNEL  
REGIONS

Appl. No.: 09/599,141

Filing Date: 06/22/2000

Examiner: Roman, A.

Art Unit: 2812

**CERTIFICATE OF MAILING**  
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, PO Box 1450, Alexandria, Virginia 22313-1450, on the date below.

Fay, A. L. Clark  
(Printed Name)

(Signature)

August 25, 2003  
(Date of Deposit)

**TRANSMITTAL OF FORMAL DRAWINGS**

Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

ATTENTION: DRAWING REVIEW BRANCH

Sir:

Transmitted herewith are the formal drawings (2 sheets, Figures 1, 2, 3, 4, 5 and 6) for the above-identified application. The Official Draftsperson is respectfully requested to approve these drawings for entry into the application.

Respectfully submitted,

Date

8-28-03

By

Joseph N. Ziebert  
(Signature)

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**Amendments to the Drawings:**

Please substitute the attached 2 sheets (Figs. 1-6) of formal drawings for the informal drawings originally filed with the application. A separate Transmittal of Formal Drawings is submitted.

The drawing sheets attached in connection with the above-identified application containing Figures 1-6 are being presented as a new formal drawing sheets to be substituted for the previously submitted drawing sheet or sheets.